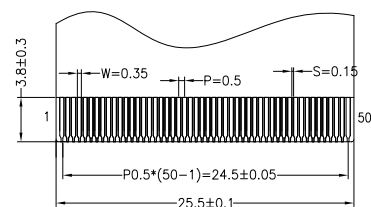
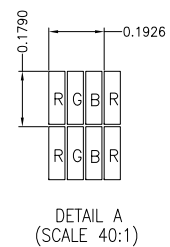
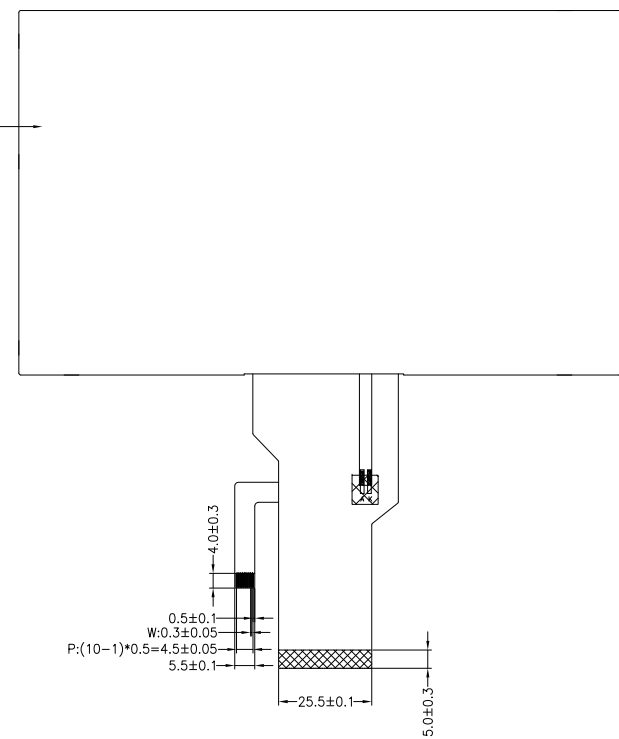
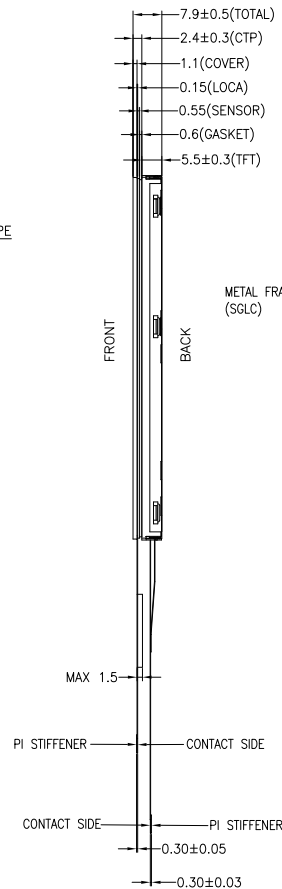
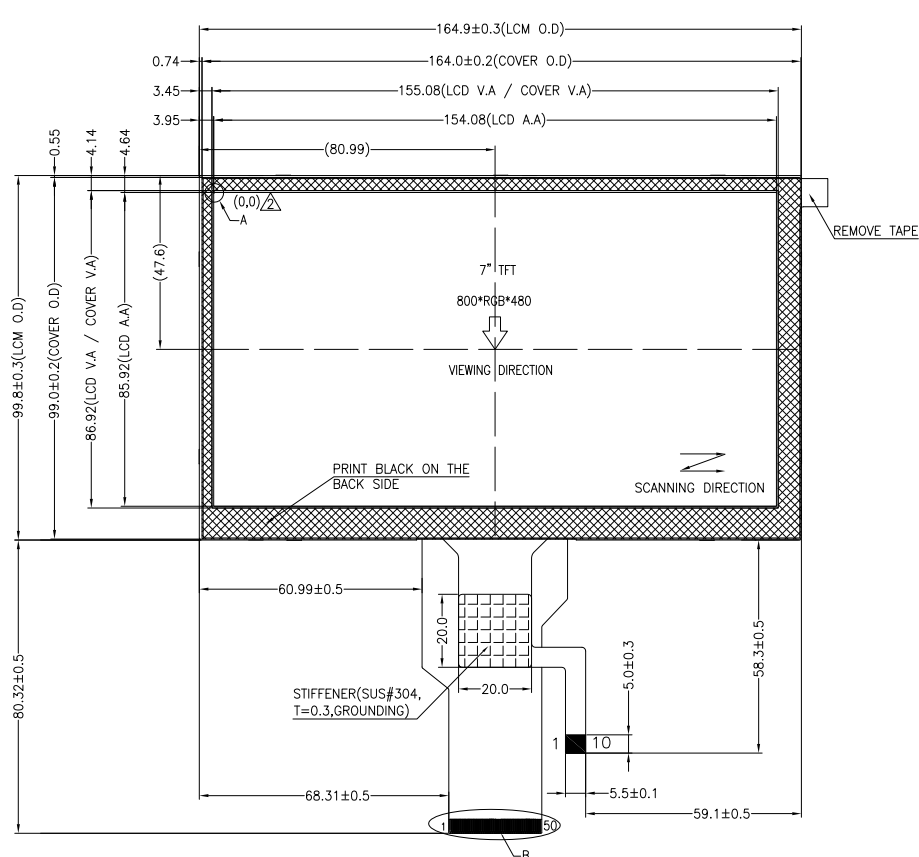


PIN ASSIGNMENT FOR CTP		PIN ASSIGNMENT FOR LCM	
NO.	SYMBOL	NO.	SYMBOL
1	RST	1	LED+
2	VCC	2	LED+
3	VSS	3	LED-
4	INT	4	LED-
5	SDA	5	GND
6	SCL	6	VCOM
7	NC	7	DVDD
8	NC	8	MODE
9	NC	9	DE
10	NC	10	VS
11	HS	12	B7
12	B7	13	B6
13	B6	14	B5
14	B5	15	B4
15	B4	16	B3
16	B3	17	B2
17	B2	18	B1
18	B1	19	B0
19	B0	20	G7
20	G7	21	G6
21	G6	22	G5
22	G5	23	G4
23	G4	24	G3
24	G3	25	G2
25	G2	26	G1
26	G1	27	G0
27	G0	28	R7
28	R7	29	R6
29	R6	30	R5
30	R5	31	R4
31	R4	32	R3
32	R3	33	R2
33	R2	34	R1
34	R1	35	R0
35	R0	36	GND
36	GND	37	DCLK
37	DCLK	38	GND
38	GND	39	L/R
39	L/R	40	U/D
40	U/D	41	VGH
41	VGH	42	VCL
42	VCL	43	AVDD
43	AVDD	44	RESET
44	RESET	45	NC
45	NC	46	VCOM
46	VCOM	47	DITHB
47	DITHB	48	GND
48	GND	49	NC
49	NC	50	NC



- NOTES FOR TFT:
1. DISPLAY TYPE: TFT/TRANSMISSIVE/NORMALLY WHITE
  2. OPERATING VOLTAGE: DVDD=3.3V
  3. VIEWING DIRECTION: 12 O'CLOCK
  4. DRIVER IC: HX8264-E00+HX8664-B00 or EK9713CA+EK73002ACGB
  5. OPERATING TEMP.: -20°C ~ 70°C
  6. STORAGE TEMP.: -30°C ~ 80°C
  7. LED BACKLIGHT: 21-CHIP WHITE LED, IF=140mA, VF=9.6V±1.2V
  8. SURFACE LUMINANCE: 350CD/M<sup>2</sup>(TYP.)
  9. TOP POLARIZER SURFACE TREATMENT: ANTI-GLARE

- NOTES FOR CTP:
1. 7.0 INCH PROJECTIVE CAPACITIVE TOUCH PANEL
  2. OPERATING VOLTAGE: VCC=3.3V
  3. COVER GLASS + SENSOR GLASS + FPCA
  4. IC DRIVER: ILI2511
  5. INTERFACE: IIC
  6. MULTI FINGER: UP TO 5
  7. HARDNESS OF COVER SURFACE: 6H

- OTHERS:
1. GENERAL TOLERANCE: ±0.3mm
  2. ROHS COMPLIANT

VER.	REVISED DESCRIPTION	REVISER	DATE
03	MODIFY THE IC DRIVER OF TFT FROM HX8264+HX8664 TO HX8264-E00+HX8664-B00 or EK9713CA+EK73002ACGB	CAROL	2021.10.11
02	CORRECT THE POSITION OF ORIGIN COORDINATES	RITA	2019.08.29
01	FIRST ISSUE	RITA	2019.05.09

**MIT** MULTI-INNO TECHNOLOGY CO., LTD.

DRAWN BY:	RITA	2019.05.09		MODULE P/N:
CHECKED BY:			SCALE : 1/1	MI0700S6T-1CP4
APPROVED BY:			UNIT: mm	DESCRIPTION:
VERSION NO:	03		SHEET NO.: 1/1	TFT + CTP MODULE